

FIG. 1

second operand 11B

first operand 11A

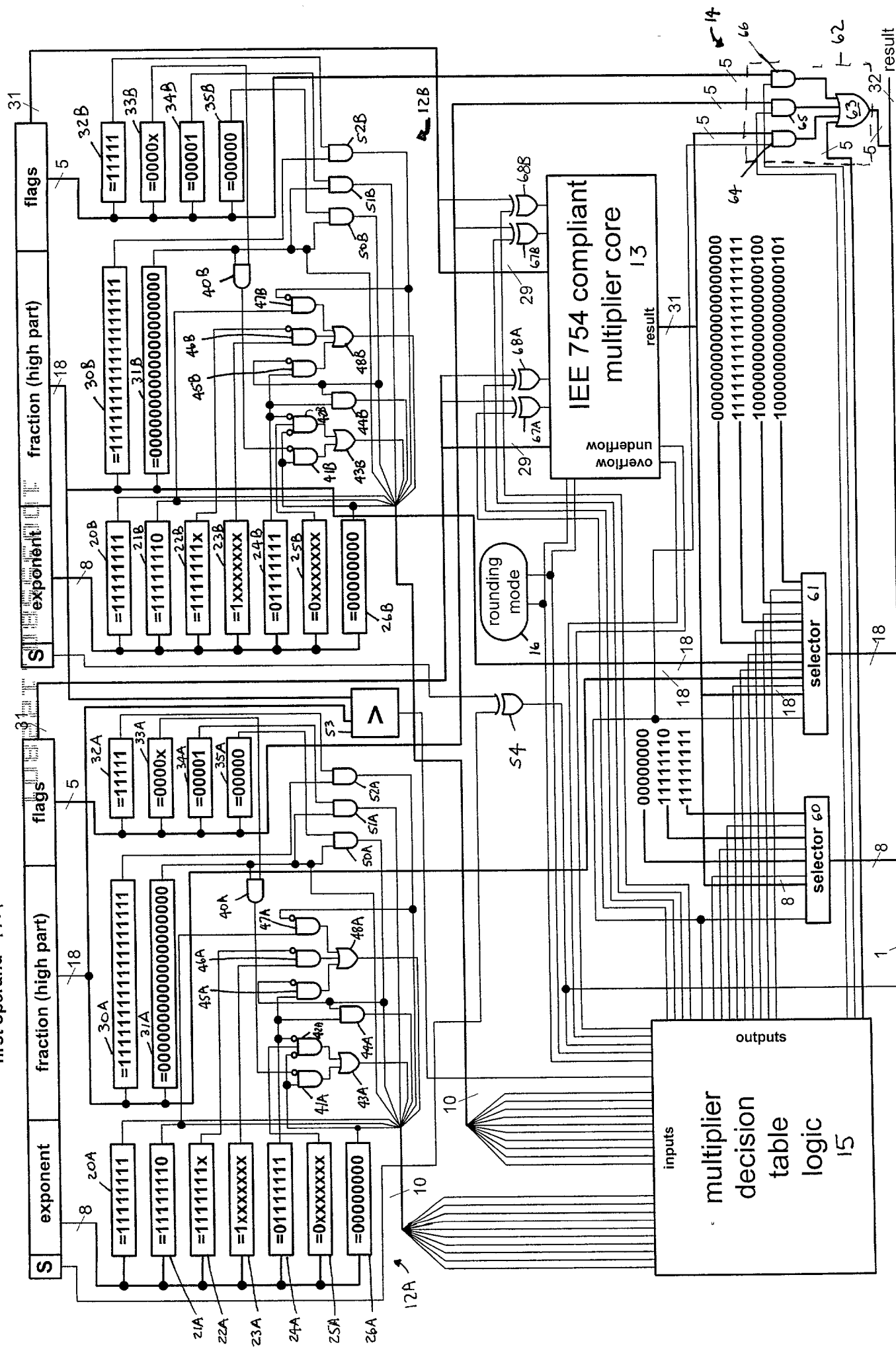


FIG. 1

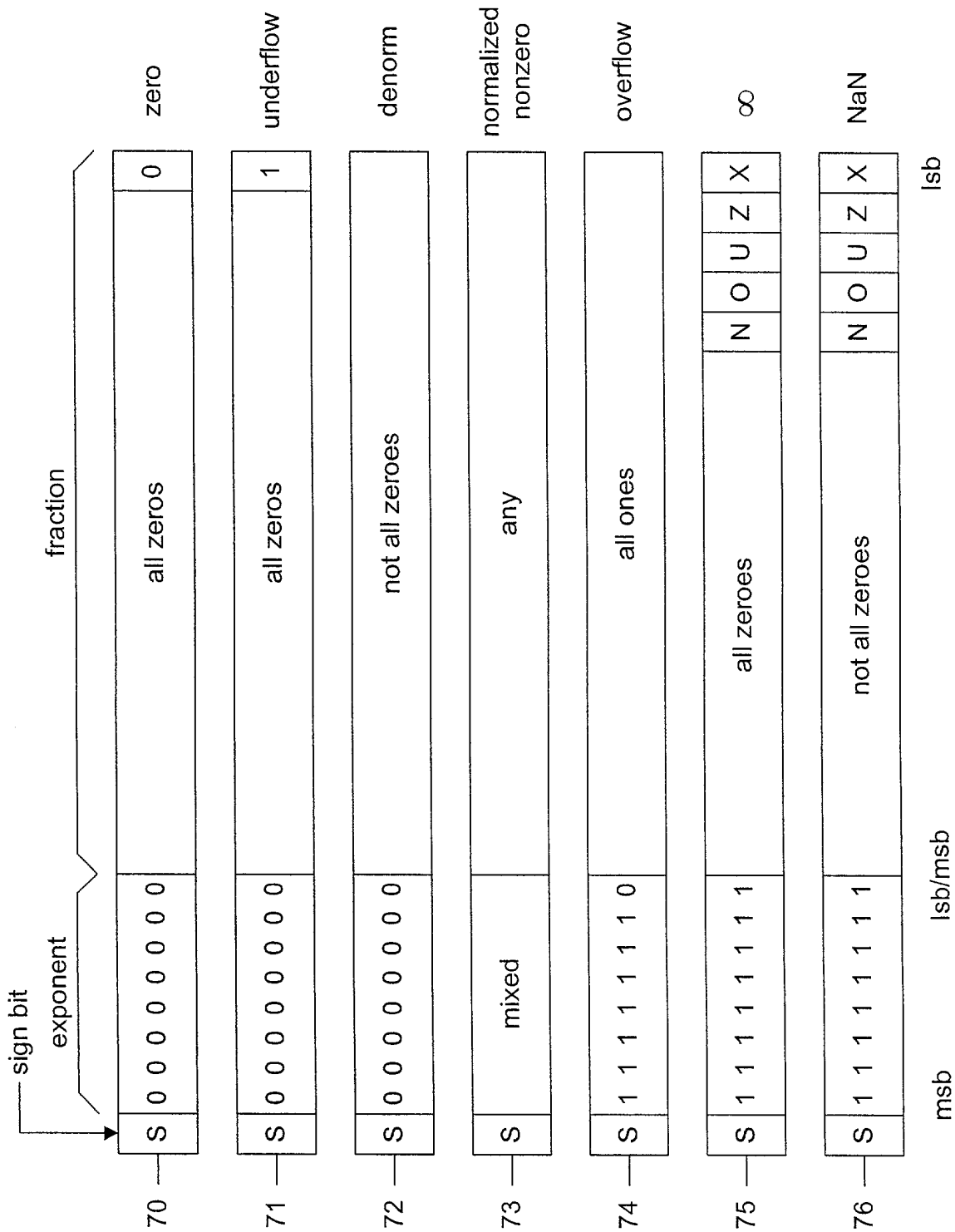


FIG. 2

TABLE 1. OPERATOR

*	-∞	-OV	-Q	-1	-S	-UN	-0	+0	+UN	+S	+1	+Q	+OV	+∞	NaN
-∞	(a)	(b)	(c)	(c)	(c)	(d)	(e)	(f)	(g)	(h)	(h)	(h)	(i)	(j)	(k)
-OV	(b)	+OV	+OV	+OV	(l)	(m)	+0	-0	(n)	(o)	-OV	-OV	-OV	(i)	(p)
-P	(c)	+OV	(q)	(q)	(q)	(r)	+0	-0	(s)	(t)	(t)	(t)	-OV	(h)	(u)
-1	(c)	+OV	(q)	(q)	(q)	+UN	+0	-0	-UN	(t)	(t)	(t)	-OV	(h)	(u)
-R	(c)	(l)	(q)	(q)	(q)	+UN	+0	-0	-UN	(t)	(t)	(t)	(v)	(h)	(u)
-UN	(d)	(m)	(r)	+UN	+UN	+UN	+0	-0	-UN	-UN	-UN	(w)	(n)	(g)	(x)
-0	(e)	+0	+0	+0	+0	+0	+0	-0	-0	-0	-0	-0	-0	(f)	(u)
+0	(f)	-0	-0	-0	-0	-0	-0	+0	+0	+0	+0	+0	+0	(e)	(u)
+UN	(g)	(n)	(s)	-UN	-UN	-UN	-0	+0	+UN	+UN	+UN	(y)	(m)	(d)	(x)
+R	(h)	(o)	(t)	(t)	(t)	-UN	-0	+0	+UN	(q)	(q)	(q)	(z)	(c)	(u)
+1	(h)	-OV	(t)	(t)	(t)	-UN	-0	+0	+UN	(q)	(q)	(q)	+OV	(c)	(u)
+P	(h)	-OV	(t)	(t)	(t)	(w)	-0	+0	(y)	(q)	(q)	(q)	+OV	(c)	(u)
+OV	(i)	-OV	-OV	-OV	(v)	(n)	-0	+0	(m)	(z)	+OV	+OV	+OV	(b)	(p)
+∞	(j)	(i)	(h)	(h)	(h)	(g)	(f)	(e)	(d)	(c)	(c)	(c)	(b)	(a)	(k)
NaN	(k)	(p)	(u)	(u)	(u)	(x)	(u)	(u)	(x)	(u)	(u)	(u)	(p)	(k)	(@)

FIG. 3

--10----	[NaN]	--10----	[NaN]	0	0000	001	100000	11	00000	[NaN	op1	f1		f2]	
--10----	[NaN]	--10----	[NaN]	0	0000	0	001	010000	11	00000	[NaN	op2	f1		f2]
--11----	[NaN]	--11----	[Inf]	-	0000	0	001	100000	11	00000	[NaN	op1	f1		f2]
-1----1--	[NaN]	-1----1--	[OV]	-	0000	0	001	100000	10	01001	[NaN	op1	f1		ox]
-----1	[NaN]	-----1	[Q]	-	0000	0	001	100000	10	00000	[NaN	op1	f1		ox]
-----1-	[NaN]	-----1-	[1]	-	0000	0	001	100000	10	00000	[NaN	op1	f1]		
-----1-	[NaN]	-----1-	[S]	-	0000	0	001	100000	10	00000	[NaN	op1	f1]		
1----1--	[NaN]	1----1--	[UN]	-	0000	0	001	100000	10	00101	[NaN	op1	f1		ux]
1---1----	[NaN]	1---1----	[0]	-	0000	0	001	100000	10	00000	[NaN	op1	f1]		
* "0355601"															
--10----	[NaN]	--10----	[NaN]	0	0000	0	001	010000	11	00000	[NaN	op1	f1		f2]
--11----	[Inf]	--11----	[Inf]	-	0000	0	001	001000	11	00000	[Inf	f1		f2]	
-1----1--	[Inf]	-1----1--	[OV]	-	0000	0	001	001000	10	00000	[Inf	f1		ox]	
-----1	[Inf]	-----1	[Q]	-	0000	0	001	001000	10	00000	[Inf	f1]			
-----1-	[Inf]	-----1-	[1]	-	0000	0	001	001000	10	00000	[Inf	f1]			
-----1-	[Inf]	-----1-	[S]	-	0000	0	001	001000	10	00000	[Inf	f1]			
1----1--	[Inf]	1----1--	[UN]	-	0000	0	001	001000	10	00101	[Inf	f1		ux]	
1--1----	[Inf]	1--1----	[0]	-	0000	0	010	000010	10	10000	[0*	Inf	f1		n]
1--1----	[Inf]	1--1----	[0]	-	0000	0	001	001000	10	00000	[Inf	f1]			
1--1----	[Inf]	1--1----	[0]	-	0000	0	000	001000	00	00000	[0]				
1--1----	[Inf]	1--1----	[0]	-	0000	0	000	001000	00	00000	[0]				
1--1----	[Inf]	1--1----	[0]	-	0000	0	000	001000	00	00000	[Inf	f1]			
* "UN*OV" noux]															
--10----	[NaN]	--10----	[NaN]	0	0000	0	001	010000	01	01001	[NaN	op2	f1		ox]
--11----	[OV]	--11----	[Inf]	-	0000	0	001	001000	01	01001	[Inf	f2		ox]	
-1----1--	[OV]	-1----1--	[OV]	-	0000	0	010	000100	00	11111	[OV]				
-----1	[OV]	-----1	[Q]	-	0000	0	010	000100	00	11111	[OV]				
-----1-	[OV]	-----1-	[1]	-	0000	0	010	000100	00	11111	[OV]				
-----1-	[OV]	-----1-	[S]	-	0000	0	010	000100	00	11111	[OV]				
-----1-	[OV]	-----1-	[S]	-	0000	0	010	000100	00	11111	[OV]				
-----1-	[OV]	-----1-	[S]	-	0100	1	000	000000	00	00000	[HUGE*S]				
-----1-	[OV]	-----1-	[S]	-	0100	1	000	000000	00	00000	[HUGE*S]				
-----1-	[OV]	-----1-	[S]	-	0000	0	010	000100	00	11111	[OV]				
1----1----	[OV]	1----1----	[UN]	-	0000	0	010	000001	00	11111	[UN*OV"			noux]	
1--1----	[OV]	1--1----	[UN]	-	0000	0	010	000100	00	11111	[OV]				
1--1----	[OV]	1--1----	[UN]	-	0000	0	100	001000	00	00001	[UN]				
1--1----	[OV]	1--1----	[UN]	-	0000	0	100	001000	00	00001	[UN]				
1--1----	[OV]	1--1----	[UN]	-	0000	0	010	000100	00	11111	[OV]				
1--1----	[OV]	1--1----	[0]	-	0000	0	001	001000	00	00000	[0]				

FIG. 4A

[P]	--1		[NaN]	- - - -	*	0000 0 001 010000 01	00000	[NaN op2 f2]
[P]	--1		[Inf]	--1		0000 0 001 001000 01	00000	[Inf f2]
[P]	-1---		[OV]	-1---		0000 0 010 000100 11	11111	[OV]
[P]	-----	1	[Q]	- - - 0-	*	0000 1 000 000000 00	00000	[P*Q]
[P]	-----	1	[Q]	- - - 1-	*	0000 0 010 000100 00	11111	[OV]
[P]	-----	1-	[1]	- - - -	*	0000 1 000 000000 00	00000	[P*1]
[P]	-----	1-	[S]	- - - -	*	0000 1 000 000000 00	00000	[P*S]
[P]	1---1		[UN]	- 0- - -	*	0000 0 100 001000 10	00001	[UN]
[P]	1---1		[UN]	- 10 0 --	*	0011 1 000 000000 00	00000	[P*TINY]
[P]	1---1		[UN]	- 10 1 --	*	0000 0 100 001000 00	00001	[UN]
[P]	1---1		[UN]	- 11 0 --	*	0000 0 100 001000 00	00001	[UN]
[P]	1---1		[UN]	- 11 1 --	*	0011 1 000 000000 00	00000	P*TINY]
[P]	1---1		[1]	- - - -	*	0000 0 100 001000 00	00000	[0]

1- -----	[1] --10-----	[NaN]	- -- --	*	0000 0 001 010000 01 00000	[NaN op2 f2]
1- -----	[1] --11-----	[Inf]	- -- --	*	0000 0 001 001000 01 00000	[Inf f2]
1- -----	[1] -1-----1--	[OV]	- -- --	*	0000 0 010 000100 00 11111	[OV]
1- -----	[1] -----1	[Q]	- -- --	*	0000 1 000 000000 00 00000	[1*Q]
1- -----	[1] -----1-	[1]	- -- --	*	0000 1 000 000000 00 00000	[1*1]
1- -----	[1] -----1--	[S]	- -- --	*	0000 1 000 000000 00 00000	[1*S]
1- -----	[1] 1-----1--	[UN]	- -- --	*	0000 0 100 001000 00 00001	[UN]
1- -----	[1] 1-----1--	[0]	- -- --	*	0000 0 100 001000 00 00000	[0]

[R]	--10-----	[NaN]	- - - - -	*	0000	0	001	010000	01	00000	[NaN op2 f2]
[R]	--11-----	[Inf]	- - - - -	*	0000	0	001	001000	01	00000	[Inf f2]
[R]	-1---1---	[OV]	- 0- - - -	*	0000	0	010	000100	00	11111	[OV]
[R]	-1---1---	[OV]	- 10 0 - -	*	0000	0	010	000100	00	11111	[OV]
[R]	-1---1---	[OV]	- 10 1 - -	*	0001	1	000	000000	00	00000	[R*HUGE]
[R]	-1---1---	[OV]	- 11 0 - -	*	0001	1	000	000000	00	00000	[R*HUGE]
[R]	-1---1---	[OV]	- 11 1 - -	*	0000	0	010	000100	00	11111	[OV]
[R]	-----1	[Q]	- - - - -	*	0000	1	000	000000	00	00000	[R*Q]
[R]	-----1-	[1]	- - - - -	*	0000	1	000	000000	00	00000	[R*1]
[R]	-----1-	[S]	- - - -0	*	0000	1	000	000000	00	00000	[R*S]
[R]	-----1-	[S]	- - - -1	*	0000	0	100	001000	00	00001	[UN]
[R]	1---1---	[UN]	- - - - -	*	0000	0	100	001000	00	00001	[UN]
[R]	1---1---	[0]	- - - - -	*	0000	0	100	001000	00	00000	[0]

FIG. 4B

1---1---	[UN]	--10----	[NaN]	- - - -	*	0000 0 001 010000 01 00101	[NaN op2 f1 ux]
1---1---	[UN]	--11----	[Inf]	- - - -	*	0000 0 001 001000 01 00101	[Inf f2 ux]
1---1---	[UN]	-1---1---	[OV]	- 0- - -	*	0000 0 010 000001 00 11101	["UN*OV" noux]
1---1---	[UN]	-1---1---	[OV]	- 10 0 -	*	0000 0 010 000100 00 11111	[OV]
1---1---	[UN]	-1---1---	[OV]	- 10 1 -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-1---1---	[OV]	- 11 0 -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-1---1---	[OV]	- 11 1 -	*	0000 0 010 000100 00 11111	[OV]
1---1---	[UN]	-----1	[Q]	- 0- - -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-----1	[Q]	- 10 0 -	*	1100 1 000 000000 00 00000	[TINY*Q]
1---1---	[UN]	-----1	[Q]	- 10 1 -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-----1	[Q]	- 11 0 -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-----1	[Q]	- 11 1 -	*	1100 1 000 000000 00 00000	[TINY*Q]
1---1---	[UN]	-----1-	[1]	- - - -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	-----1-	[S]	- - - -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	1---1---	[UN]	- - - -	*	0000 0 100 001000 00 00001	[UN]
1---1---	[UN]	1---1---	[0]	- - - -	*	0000 0 100 001000 00 00000	[0]

1---1---	[0]	--10----	[NaN]	- - - -	*	0000 0 001 010000 01 00000	[NaN op1 f2]
1---1---	[0]	--11----	[Inf]	- 0- - -	*	0000 0 001 000010 01 10000	["0*Inf" f2 n]
1---1---	[0]	-1---1---	[Inf]	- 10 0 -	*	0000 0 001 001000 01 00000	[Inf f2]
1---1---	[0]	-1---1---	[Inf]	- 10 1 -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	-1---1---	[Inf]	- 11 0 -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	-1---1---	[Inf]	- 11 1 -	*	0000 0 001 001000 01 00000	[Inf f2]
1---1---	[0]	-1---1---	[OV]	- - - -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	-----1	[Q]	- - - -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	-----1-	[1]	- - - -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	-----1-	[S]	- - - -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	1---1---	[UN]	- - - -	*	0000 0 100 001000 00 00000	[0]
1---1---	[0]	1---1---	[0]	- - - -	*	0000 0 100 001000 00 00000	[0]

FIG. 4C